

FIG.1

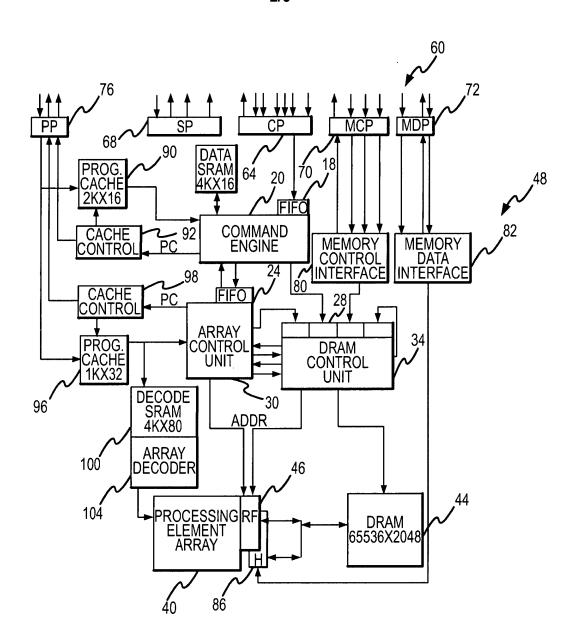


FIG.2

DYNAMIC ARRAY INSTRUCTION DECODER

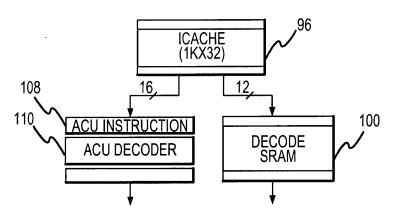


FIG.3

DYNAMIC ARRAY INSTRUCTION DECODER WITH FULL WIDTH BYPASS

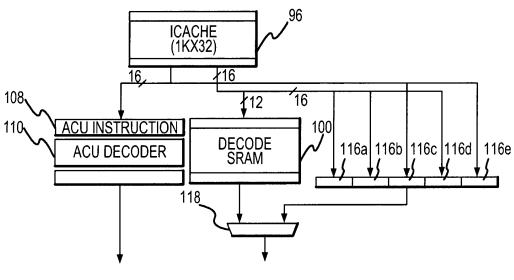


FIG.4

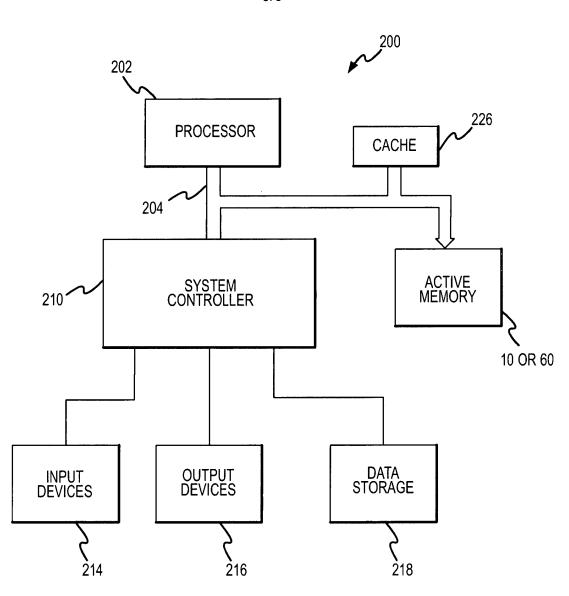


FIG.5